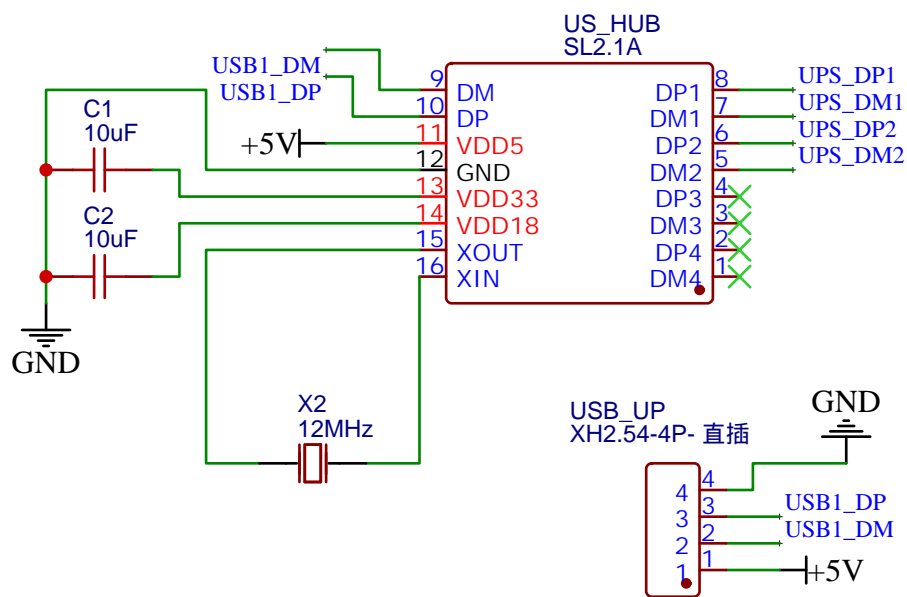
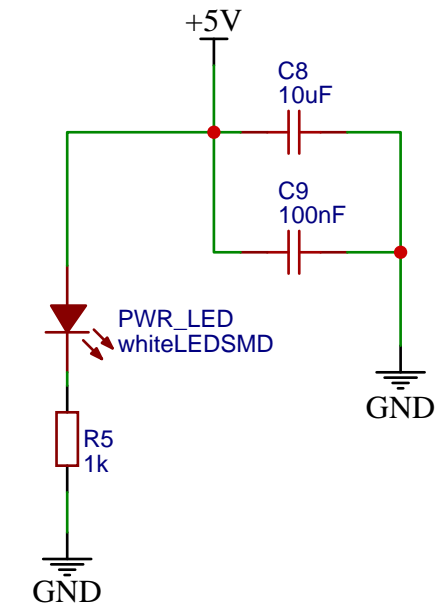
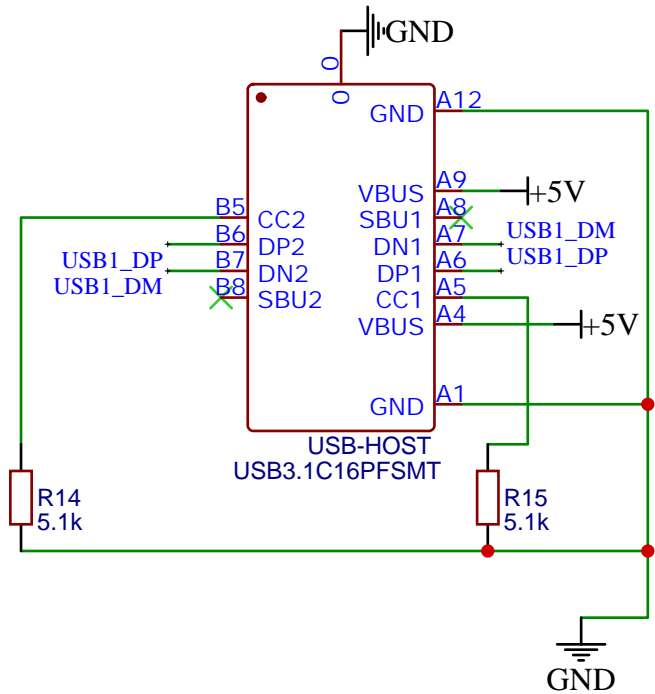


Upstream USB Port
The port connecting to the KVM control SBC (e.g. Orange Pi)

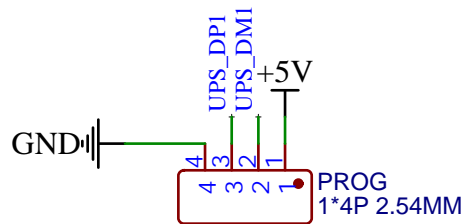
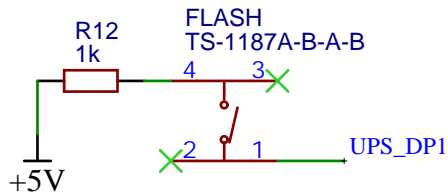
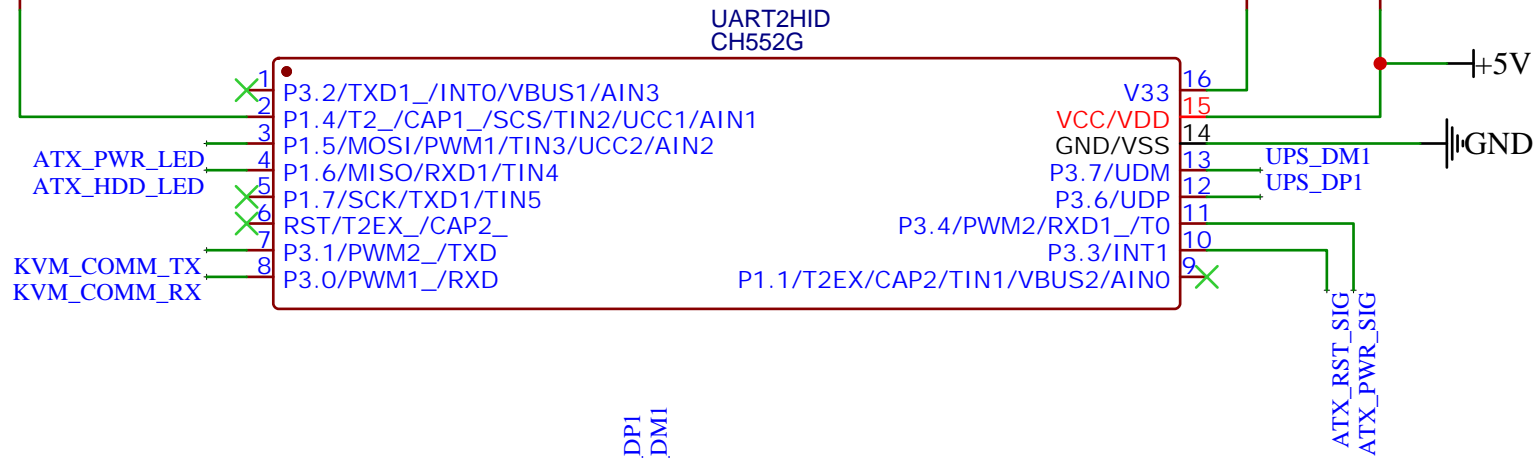
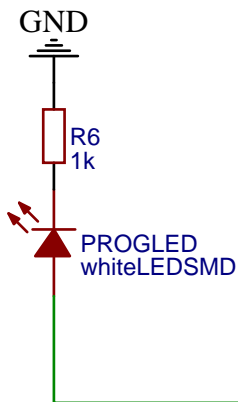
Power Ripple Filter



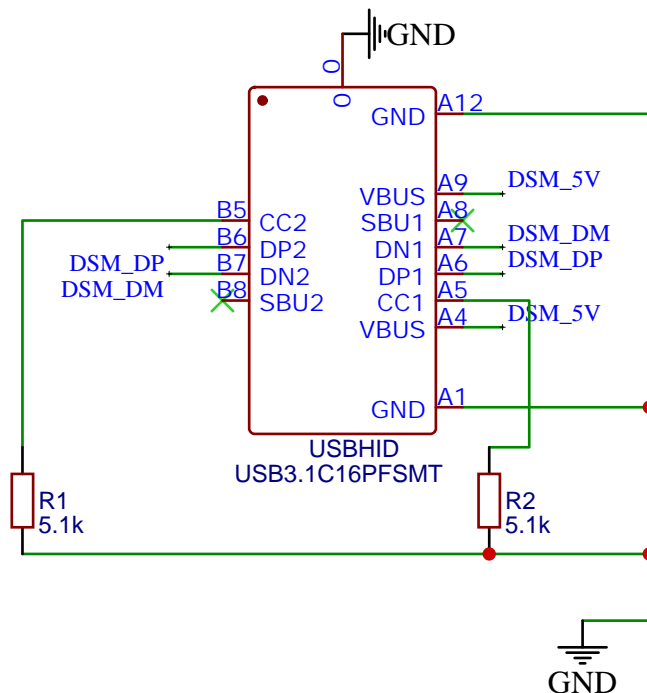
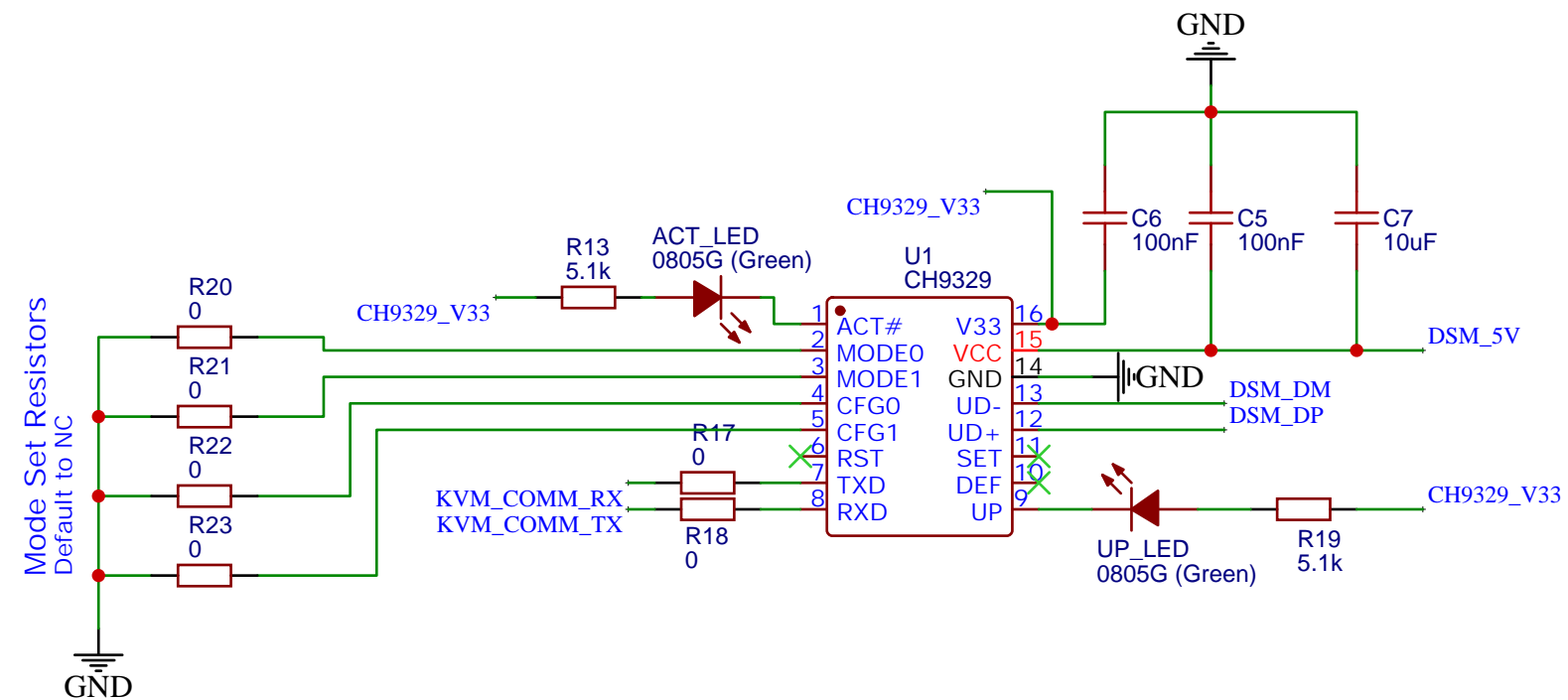
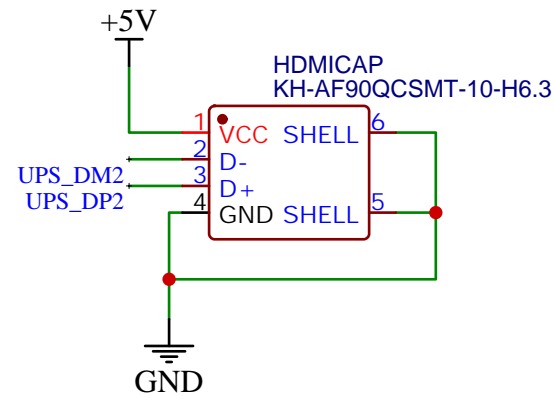
To CH552G
To USB HDMI Captuer




USB to UART
for KVM SBC sending ctrl-signal to KVM



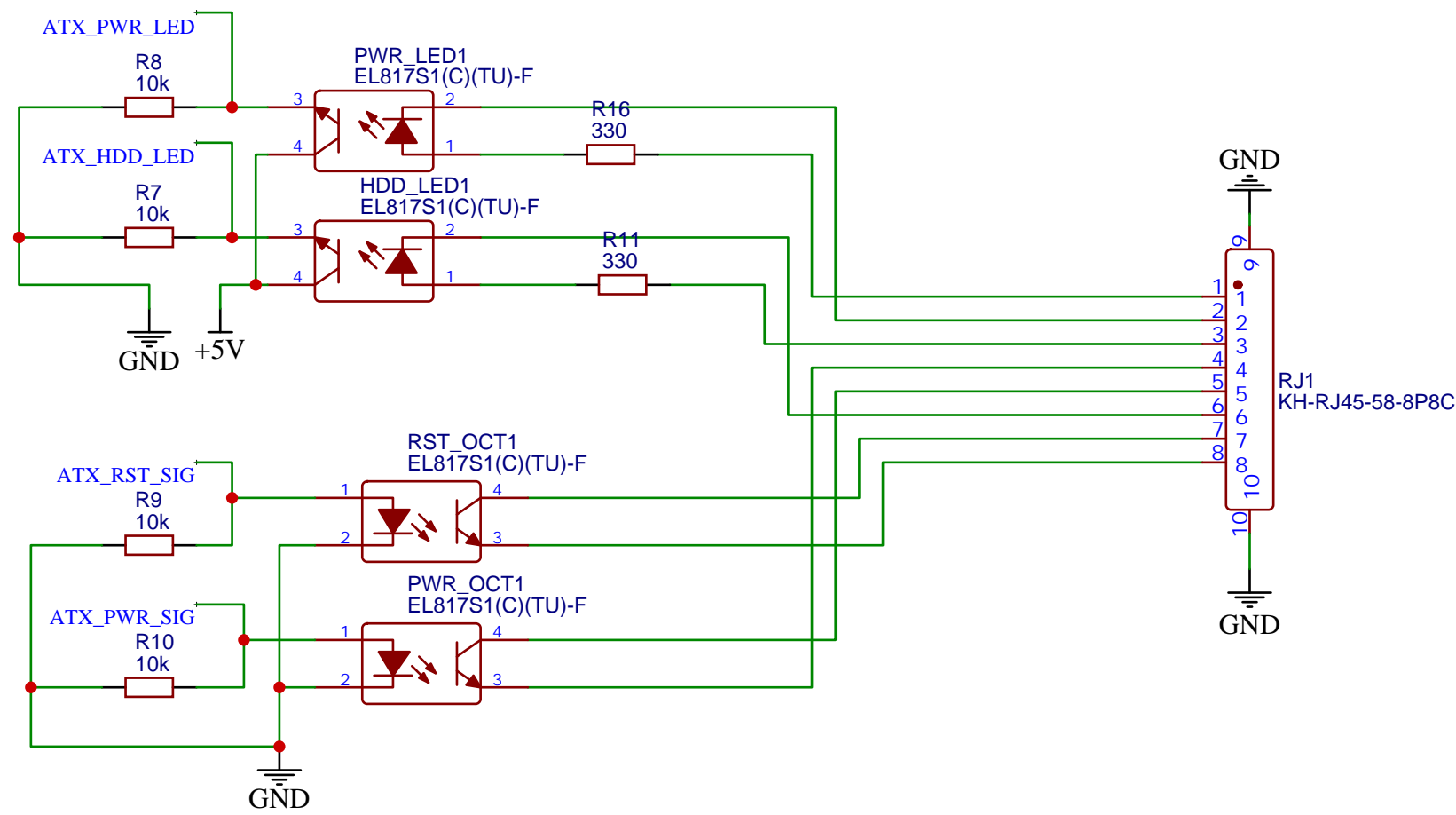
TITLE: KVM Hub		REV: 1.0
EasyEDA	Company: RemdesKVM	Sheet: 1/1
	Date: 2025-02-18	Drawn By: tobychui



TITLE: Guest Side		REV: 1.0
	Company: imuslab	Sheet: 1/1
	Date: 2025-02-20 Drawn By: tobychui	

ATX Power Control

Notes: ATX_PWR_LED and ATX_HDD_LED default to LOW, active HIGH
This design should be compatible with PiKVM ATX Board Pinsout (Not Tested)



TITLE: ATX Rev2		REV: 1.0
EasyEDA	Company: imuslab	Sheet: 1/1
	Date: 2025-04-20	Drawn By: tobychui