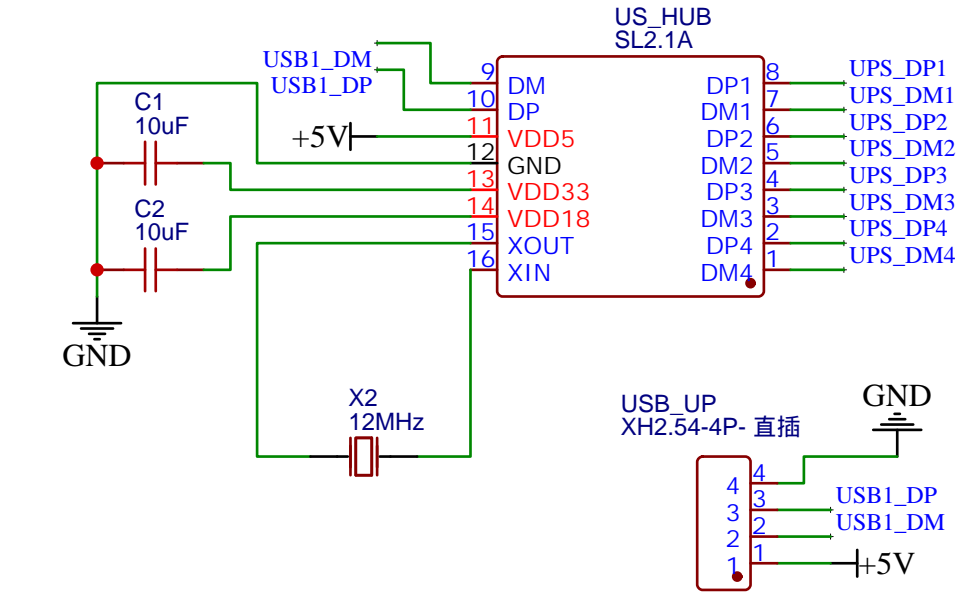
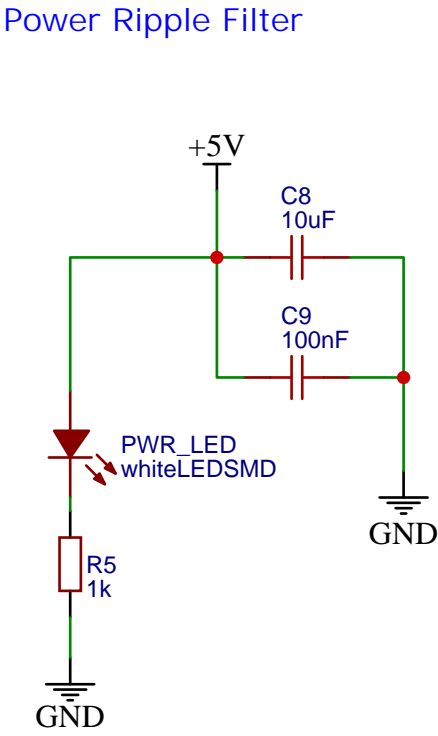
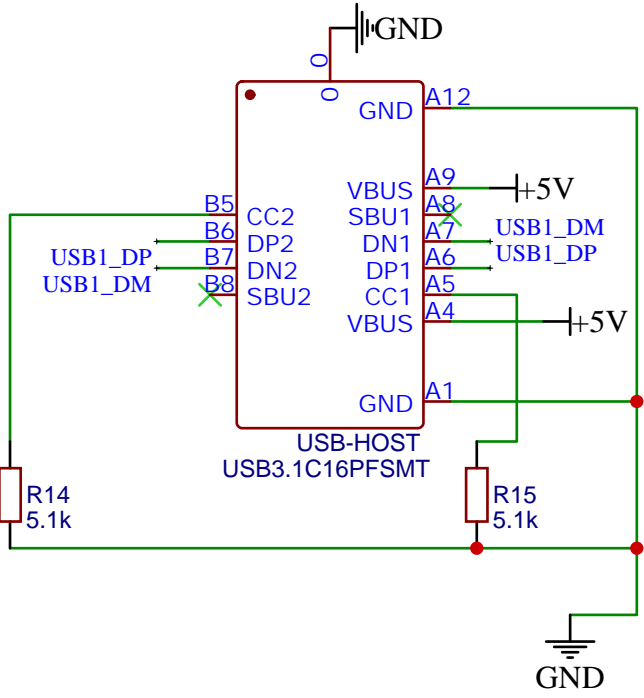


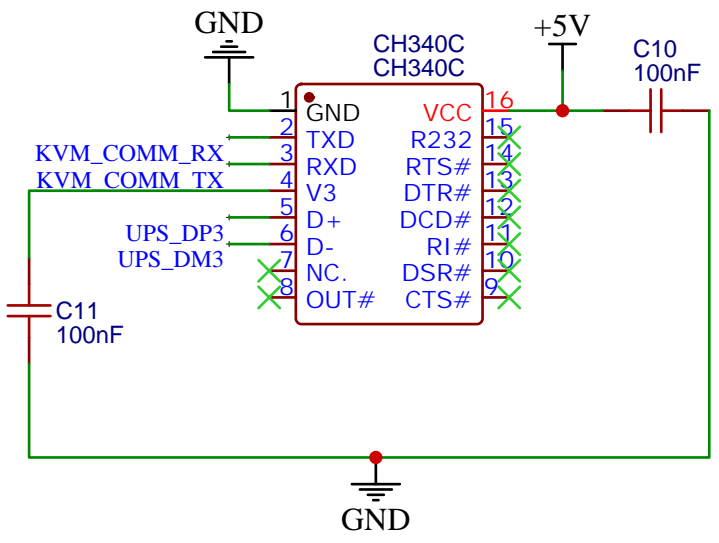
Upstream USB Port
The port connecting to the KVM control SBC (e.g. Orange Pi)

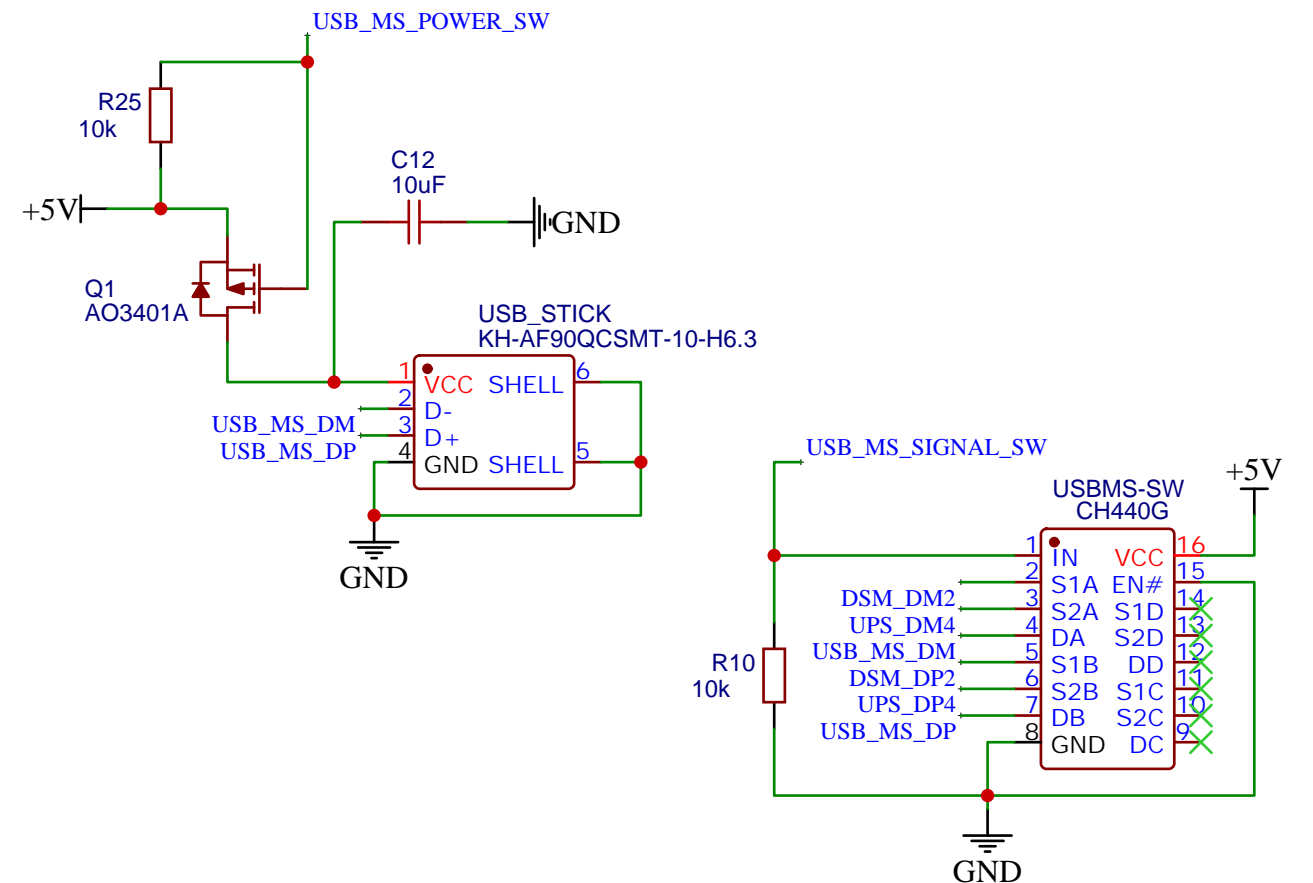
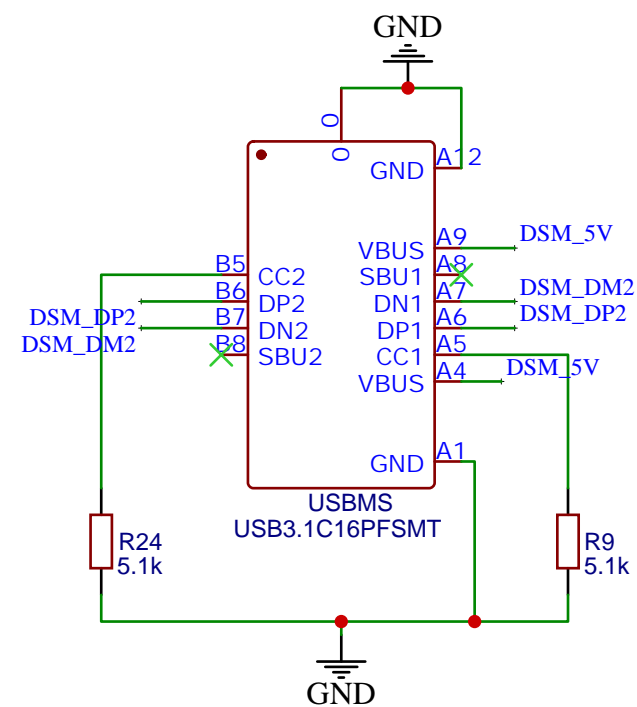



To CH552G
To USB HDMI Captuer
To CH340C & CH9329
To USB Mass Storage SW



USB to UART
for KVM SBC sending ctrl-signal to KVM





TITLE: Guest Side		REV: 1.0
	Company: imuslab	Sheet: 1/1
	Date: 2025-02-20 Drawn By: tobychui	

ATX Power Control

Notes: ATX_PWR_LED and ATX_HDD_LED default to LOW, active HIGH

This design should be compatible with PiKVM ATX Board Pinsout (Not Tested)

